

5414662

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				Subclass
				ISSUE CLASSIFICATION

UTILITY
SERIAL
NUMBER 08/147,038PATENT DATE
MAY 09 1995PATENT
NUMBER 5414662

SERIAL NUMBER 08/147,038	FILING DATE 11/04/93	CLASS 365	SUBCLASS	GROUP ART UNIT 2502	EXAMINER 252
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CONTINUING DATA***

VERIFIED THIS APPLN IS A CON OF 07/680,747 04/05/91 *abandoned*.*T.D. yes*

FOREIGN/PCT APPLICATIONS***

VERIFIED UNITED KINGDOM 9007789.2 04/06/90
UNITED KINGDOM 9107164.7 04/05/91*T.D. yes*

***** SMALL ENTITY *****

Foreign priority claimed 35 USC 119 conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	AS FILED →	STATE OR COUNTRY CAX	Sheets DRWGS. 3x	TOTAL CLAIMS 17	INDEP. CLAIMS 4	FILING FEE RECEIVED \$392.00	ATTORNEY'S DOCKET NO. 628,30050CX1
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TITLE METHOD FOR DRAM SENSING CURRENT CONTROL DYNAMIC RANDOM ACCESS MEMORY USING IMPERFECT ISOLATING TRANSISTORS	U.S. DEPT. OF COMM-Pat. & TM Office - PTO-436L (Rev. 10-78)
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PARTS OF APPLICATION FILED SEPARATELY	01/28/94 J. van Heege Applications Examiner	
NOTICE OF ALLOWANCE MAILED <i>1/27/94</i>	CLAIMS ALLOWED Total Claims 17 Print Claim 1	
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PREPARED FOR ISSUE		
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